Form PTO-1449 US Dept. of Commerce (REV. 8-83) PATENT & TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT			ATTY DOCKET NO. 038959.02 APPLICATION NO. Rule 53(b) Divisional Application of Application No. 09/671,354, filed							
						mber 27, 20				
(Use several sheets if necessary)			APPLICANT(S) Osamu OKUMURA, Eiji OKAMOTO, Tsuyoshi MAEDA and Toshiharu MATSUSHIMA							
			FILING I April 21,	—	FORMER GROUP 2871					
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,	5,870,157	02/1	999	SHIMADA et al.		1				
	6,084,465	07/2	000	SEIKIGUCHI		1				
	5,365,357	11/1	994	OHGAWARA et al.						
	4,964,702	10/1	990	SUGIMOTO et al.			X			
	6,140,988	10/2	000	YAMADA						
	5,587,819	12/1	996	SUNOHARA et al.						
	5,508,831	04/1	996	NAKAMURA et al.		/				
	5,122,887	06/1	992	CONNER et al.						
$A \leftarrow A$	4,560,241	12/1	985	STOLOV et al.						
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	JP-A-60-214342	10/1	995	JAPAN						
	JP-A-56-50369	05/19	981	JAPAN		· \				
-	JP-A-4-282613	10/19	992	JAPAN		\				
	JP-A-4-289818	10/19	992	JAPAN						
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	JP 3-64850	08/19		JAPAN						
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1				itle, Date, Pertinent Pages, etc.)						
4	"Color LCD Structure," IBM Technical						•			
FYAMINED	A.G. CHEN et al., "Late-News Paper:	Hologi	raphic Refl				179.			
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Form PTO-1449 US Dept. of Commerce (REV. 8-83) PATENT & TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT			ATTY DOCKET NO. 038959.02 APPLICATION NO. Rule 53(b) Divisional Application of Application No. 09/671,354, filed September 27, 2000							
(Use several sheets if necessary)			APPLICANT(S) Osamu OKUMURA, Eiji OKAMOTO, Tsuyoshi MAEDA and Toshiharu MATSUSHIMA							
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	\bot	JP 4-348323 (w/Eng. Abstract)	12/1992		JAPAN		<u> /:</u>			
		JP 5-241143 (w/Eng. Abstract)	09/1993		JAPAN		<u> </u>			
		JP 6-235920 (w/Eng. Abstract)	08/1994		JAPAN		1		1	
		JP 7-239649 (w/Eng. Abstract)	09/1	995	JAPAN		1		/	
		JP 8-87009 (w/Eng. Abstract)	04/1		JAPAN					
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		JP-A-7-13147 (w/Eng. Trans)	01/1	995	JAPAN		:			
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M		S. MITSUI et al., "Late-News Paper: E 437-440								
4		T. UCHIDA et la., "Reflective Multico No. 8, pp. 1207-1211, August 8, 1996.	lor Li	quid-Crysta	al Display," <u>IEEE Trans</u> a	ctions on Electron	Device	<u>s,</u> Vo	1. ED-33,	
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(Use several sheets if necessary)			APPLICANT(S) Osamu OKUMURA, Eiji OKAMOTO, Tsuyoshi MAEDA and Toshiharu MATSUSHIMA							
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